

IT-DSE: Invariance Risk Minimized Transfer Microarchitecture Design Space Exploration

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Introduction



Microarchitecture Design Space exploration:

- Microarchitecture determines the detailed structures of a microprocessor.
- Microarchitecture design space exploration involves finding different configurations with desired performance, power and area (PPA).

Two major challenges:

- Complexity of design spaces growing rapidly: number of potential configurations, inter-feature interactions, mixed-type parameters.
- Getting desired evaluation metric reports for one microarchitecture configuration through VLSI integration flow is timing consuming.

Previous Solutions and Limitations



Previous solution

In industry:

- Manually configure design parameters by computer architects.
- Limitation: Requires both sufficient domain knowledge and a large amount of human labor for computer architects.

In academia:

- Machine learning-based process simulation models or surrogate models.
- Limitations: Effectiveness depdends on quality of training data and model granularity.

Historical Configurations are Useful



Two facts:

- Different generations of microprocessors are developed based on a common baseline microarchitecture. Minor improvements are applied across consecutive microarchitecture generations.
- Some general relations exist for design features due to the similar microarchitecture design paradigm.

ANN-TL¹: Leveraging historical configurations for new tasks with cross-domain mixup and Artificial Neural Network (ANN) feature. **Limitation:** ignores intricate feature interactions, lacks interpretability, presumes identical design spaces for source and target tasks.

¹Jianwang Zhai, Yici Cai, and Bei Yu (2023). "Microarchitecture Power Modeling via Artificial Neural Network and Transfer Learning". In: 2023 28th Asia and South Pacific Design Automation Conference (ASP-DAC), pp. 1–6.





The characteristics of certain design features have similar or even identical effects on PPA values in different generations of a microarchitecture design.

Extract the relationship that is approximately invariant among different historical source design tasks, even equipped with different design spaces.

Transfer domain knowledge from different source tasks to the target design space exploration task.

Preliminaries

Typical Microprocessor Components





Visualization of typical microarchitecture pipeline.

Invariant Risk Minimization (IRM)²

Prediction function $f' : \mathcal{X} \to \mathcal{Y} \Rightarrow \phi_u(\cdot)$: feature extractor; $h_w(\cdot)$: regressor. *u*, *w*: parameter. **IRM:**

$$\min_{\boldsymbol{u},\boldsymbol{w}} \quad \sum_{s=1}^{s} \mathcal{R}^{s}(\boldsymbol{u},\boldsymbol{w}),$$
s.t. $\boldsymbol{w} \in \operatorname*{argmin}_{\boldsymbol{w}^{s}} \mathcal{R}^{s}(\boldsymbol{u},\boldsymbol{w}^{s}),$
(1)

$$\mathcal{R}^{s}(\boldsymbol{u}, \boldsymbol{w}) = \mathbb{E}_{X^{s}, Y^{s}}[\mathcal{L}(f'(X^{s}), Y^{s})]$$
: risk under *s*-th source task.
 $\mathcal{L}(\cdot)$: the loss function.
Simplified IRMv1:

$$\min_{\boldsymbol{u},\boldsymbol{w}} \sum_{s=1}^{S} \mathcal{R}^{s}(\boldsymbol{u},\boldsymbol{w}) + \lambda ||\nabla_{\boldsymbol{w}} \mathcal{R}^{s}(\boldsymbol{u},\boldsymbol{w})||^{2},$$
(2)

²Martin Arjovsky et al. (2019). "Invariant risk minimization". In: *arXiv preprint arXiv:1907.02893*. 10/30

Problem Definition (1)



Source Task

Previously explored microarchitecture designs tasks. Composed of the explored sample dataset \mathcal{D}^s containing n_s parameter vectors $X^s = [\boldsymbol{x}_1^s, \dots, \boldsymbol{x}_{n_s}^s]^\top \in \mathbb{R}^{n_s \times d_s}$ and the evaluated PPA vectors $Y^s = [\boldsymbol{y}_1^s, \dots, \boldsymbol{y}_{n_s}^s] \in \mathbb{R}^{n_s \times 3}$.

Target Task

The new microarchitecture design space exploration task with only the set of legal parameter configuration vectors $X^t = [\mathbf{x}_1^t, \dots, \mathbf{x}_{n_t}^t]^\top \in \mathbb{R}^{n_t \times d_t}$ available.

Pareto Optimality

For a multi-objective minimization problem with *M* objectives, x_1 is deemed to dominate solution x_2 ($x_1 \succeq x_2$) if

$$f_m(\mathbf{x}_1) \leq f_m(\mathbf{x}_2), \forall m \in \{1, ..., M\}, f_k(\mathbf{x}_1) < f_k(\mathbf{x}_2), \exists k \in \{1, ..., M\}.$$
(3)

Pareto-optimal set: The collection of solutions that remain non-dominated by others.



Microarchitecture Transferring Design Space Exploration

Given *S* source tasks with explored datasets $\{\mathcal{D}^1, \mathcal{D}^2, \dots, \mathcal{D}^S\}$ and the target sample \mathcal{D}^t , the objective is to utilize the information from historical source tasks and improve the efficiency of finding a series of microprocessor design configurations *X* in the target task that forms the Pareto optimality among the associated subset of $Y \subset \mathcal{Y}$, so that $X = \{\mathbf{x} | \mathbf{x} \succeq \mathbf{x}', \forall \mathbf{x}' \in X^t\}, Y = \{f(\mathbf{x}) | X \in X^t\}.$

IT-DSE Algorithm

Workflow





Workflow of proposed IT-DSE.

- Accommodate varying design spaces: FT-Transformer ensemble
- Knowledge fusion and warm start new task: pre-training with invariant risk minimization paradigm

Customized Surrogate Model with Transformer





(a) FT-Transformer architecture. Firstly, Feature Tokenizer transforms input parameters into embeddings. The embedding is then processed by the Transformer module. (b) One Transformer layer.

$$\begin{aligned} \boldsymbol{t}_{i}^{num} &= \boldsymbol{x}_{i}^{num} \cdot \boldsymbol{w}_{i}^{num} + \boldsymbol{b}_{i}^{num}, \quad (4) \\ \boldsymbol{t}_{j}^{cat} &= \boldsymbol{e}_{j}^{\top} \boldsymbol{w}_{j}^{cat} + \boldsymbol{b}_{j}^{cat}, \quad (5) \\ \boldsymbol{T} &= \mathrm{stack}([\mathtt{CLS}], \boldsymbol{t}_{1}^{num}, \dots \boldsymbol{t}_{d_{n}}^{num}, \boldsymbol{t}_{1}^{cat}, \dots, \boldsymbol{t}_{d_{c}}^{cat}). \quad (6) \\ & 15/30 \end{aligned}$$



Comprehensive union of extracted features:

Source task 1: $x^1 = [x_1, x_2]$, source task 2: $x^2 = [x_2, x_3, x_4]$, Embedding weight: $W = [w_1, w_2, w_3, w_4]^\top \in \mathbb{R}^{4 \times d}$, Embedding bias: $B = [b_1, b_2, b_3, b_4]^\top \in \mathbb{R}^{4 \times d}$, Transformed embeddings: $T^1 = \text{stack}([\text{CLS}], t_1, t_2), T^2 = \text{stack}([\text{CLS}], t_2, t_3, t_4)$.

Bayesian Invariant Risk Minimized Feature Extraction



Focusing more on task-invariant features.

For *s*-th source task, original data: $\mathcal{D}^s = \{X^s, Y^s\} = \{x^s_i, y^s_i\}_{i=1}^{n_s}$

transferred data: $\mathcal{T}^s = \{\phi_u(\mathbf{x}^s_i), \mathbf{y}^s_i\}_{i=1}^{n_s}$

collection of transformed datasets of all *S* source tasks: $T^c = \bigcup_{s=1}^{S} T^s$.

Objective for Bayesian IRM:

$$\max_{\boldsymbol{u}} \sum_{s} \mathbb{E}_{g_{\boldsymbol{u}(\boldsymbol{w})}}[\ln p(\mathcal{T}^{s}|\boldsymbol{w},\boldsymbol{u})] + \lambda \left(\mathbb{E}_{g_{\boldsymbol{u}(\boldsymbol{w})}}[\ln p(\mathcal{T}^{s}|\boldsymbol{w},\boldsymbol{u})] - \mathbb{E}_{g_{\boldsymbol{u}}^{s}(\boldsymbol{w}^{s})}[\ln p(\mathcal{T}^{s}|\boldsymbol{w}^{s},\boldsymbol{u})]\right).$$
(7)

 $g_u(w) \approx p(w|\mathcal{T}^c), g_u^s(w^s) \approx p(w^s|\mathcal{T}^s).$

First term: Encouraging *u* to retain information for data distribution fitting.

Second term: Requires transformed data distribution to be stable among different tasks with feature extractor $\phi_u(\cdot)$.

Adaptive Solution of Bayesian IRM



The actual posterior $p(w|T^c)$ and $p(w^s|T^s)$ are hard to estimate in large models. Variational inference: approximate the posterior distributions by maximizing the evidence lower bound (ELBO).

$$g_{\boldsymbol{u}}^{s}(\boldsymbol{w}^{s}) = \underset{g' \in \mathcal{G}}{\operatorname{argmax}} \mathbb{E}_{g'}[\ln p(\mathcal{T}^{s}|\boldsymbol{w}, \boldsymbol{u}) - \mathcal{D}_{\mathrm{KL}}(g'||p_{0}(\boldsymbol{w}))], \tag{8}$$
$$g_{\boldsymbol{u}}(\boldsymbol{w}) = \underset{g'}{\operatorname{argmax}} \sum_{s=1}^{S} \mathbb{E}_{g' \in \mathcal{G}}[\ln p(\mathcal{T}^{s}|\boldsymbol{w}, \boldsymbol{u}) - \mathcal{D}_{\mathrm{KL}}(g'||p_{0}(\boldsymbol{w}))], \tag{9}$$

Assume $g_u(w) = \mathcal{N}(\mu, \Sigma)$, $g_u^s(w^s)$ approaches $g_u(w)$ when more task-invariant features are extracted:

$$g_{\boldsymbol{u}}^{s}(\boldsymbol{w}^{s}) = \mathcal{N}(\boldsymbol{\mu} - \nabla_{\boldsymbol{\mu}} \mathbb{E}_{g_{\boldsymbol{u}}(\boldsymbol{w})} \ln p(\mathcal{T}^{s} | \boldsymbol{w}, \boldsymbol{u}), \boldsymbol{\Sigma}),$$
(10)

Use Monte Carlo samples to estimate penalty term in Equation (7):

$$w = \mu + \epsilon \Sigma, \quad w^s = \mu^s + \epsilon \Sigma^s.$$
 (11)



Algorithm Feature extraction with Bayesian IRM

Require: Feature extractor ϕ_u , regressor h_w , collection of data from *S* source task $\{\mathcal{D}^s\}_{s=1}^S$ **Ensure:** the learned task-invariant feature extractor ϕ_u , regressor h_w .

- 1: Initialization: prior $\phi_0 \leftarrow \mathcal{N}(\mathbf{0}, \sigma \mathbf{I})$.
- 2: repeat
- 3: **for** *s* in 1, ..., *S* **do**
- 4: Sample a batch of data $(X_{batch}^{s}, Y_{batch}^{s})$ from \mathcal{D}^{s} ;
- 5: Transform data $\mathcal{T}_{batch}^{s} \leftarrow (\phi_{u}(X_{batch}^{s}), Y_{batch}^{s});$
- 6: end for
- 7: Update $g_u(w)$;
- 8: **for** *s* in 1, ..., *S* **do**
- 9: Update $g_u^s(w^s)$;
- 10: **end for**
- 11: Sample $g_u(w)$ and $g_u^s(w^s)$;
- 12: Update *u* to maximize objective;
- 13: **until** Training finished.
- 14: Return ϕ_u , h_w ;

- ⊳ Equation (9)
- ⊳ Equation (10)
- ▷ Equation (11)▷ Equation (7)

Target Task DSE in Feature Space: Deep Ensemble Surrogate

Given *n* evaluated samples $\mathcal{D}^t = \{X^t, f'(X^t)\}$ from target task, $f'(X^t) = \{(f'(\mathbf{x}_1^t))^\top, f'((\mathbf{x}_2^t))^\top, \dots, f((\mathbf{x}_n^t))^\top\}^\top \in \mathbb{R}^{n \times 3}$, For new point \mathbf{x}_*^t , posterior distribution $p(\mathbf{y}_*^t | \mathbf{x}_*^t)$ is approximated as a Gaussian distributions:

$$p(\boldsymbol{y}_{*}^{t}|\boldsymbol{x}_{*}^{t}) = \mathcal{N}\left(\boldsymbol{\mu}_{\boldsymbol{y}_{*}^{t}|\boldsymbol{f}'(\boldsymbol{X}^{t})}, \boldsymbol{\sigma}_{\boldsymbol{y}_{*}^{t}|\boldsymbol{f}'(\boldsymbol{X}^{t})}^{2}\right),$$
(12)

The posterior mean and variance:

$$\boldsymbol{\mu}_{\boldsymbol{y}_{*}^{t}|f'(X^{t})} = \frac{1}{M} \sum_{m=1}^{M} \boldsymbol{\mu}_{\theta_{m}}(\boldsymbol{x}_{*}^{t}),$$

$$\boldsymbol{\sigma}_{\boldsymbol{y}_{*}^{t}|f'(X^{t})}^{2} = \frac{1}{M} \sum_{m=1}^{M} (\boldsymbol{\mu}_{\boldsymbol{y}_{*}^{t}|f'(X^{t})} - \boldsymbol{\mu}_{\theta_{m}}(\boldsymbol{x}_{*}^{t}))^{2}$$

$$+ \frac{1}{M} \sum_{m=1}^{M} \boldsymbol{\sigma}_{\theta_{m}}^{2}(\boldsymbol{x}_{*}^{t}).$$

$$(13)$$

Target Task DSE in Feature Space: Pareto EHVI Acquisition Function³ Hypervolume given an approximate Pareto frontier $\mathcal{P}(y)$:

$$HV_{\boldsymbol{y}_{ref}}(\mathcal{P}(\boldsymbol{y})) = \lambda_M(\cup_{\boldsymbol{y}\in\mathcal{P}(\boldsymbol{y})}[\boldsymbol{y},\boldsymbol{y}_{ref}]), \tag{15}$$

 $[y, y_{ref}]$: hyperrectangle bounded by y and reference point y_{ref} . Hypervolume improvement (HVI) for a new given point y_* given $\mathcal{P}(y)$:

$$HVI(\boldsymbol{y}_*|\mathcal{P}(\boldsymbol{y}), \boldsymbol{y}_{ref}) = V_{\boldsymbol{y}_{ref}}(\mathcal{P}(\boldsymbol{y} \cup \boldsymbol{y}_*)) - V_{\boldsymbol{y}_{ref}}(\mathcal{P}(\boldsymbol{y})).$$
(16)

EHVI Monte Carlo integration approximation with *K* sampling times:

$$EHVI(\hat{\boldsymbol{x}}) = \mathbb{E}_{P(f'(\hat{\boldsymbol{x}}_*)|\mathcal{D})} \left(HVI(f'(\hat{\boldsymbol{x}})|\mathcal{P}(\boldsymbol{y}), \boldsymbol{y}_{ref}) \right)$$

$$\approx \frac{1}{n_k} \sum_{k=1}^{K} HVI(\tilde{f}'_k(\hat{\boldsymbol{x}})|\mathcal{P}(\boldsymbol{y}), \boldsymbol{y}_{ref}).$$
(17)

 \tilde{f}'_k : *k*-th function evaluation from distribution $p(f'(\mathbf{x}|\mathcal{D}))$.

³Samuel Daulton, Maximilian Balandat, and Eytan Bakshy (2020). "Differentiable expected hypervolume improvement for parallel multi-objective Bayesian optimization". In: *Advances in Neural Information Processing Systems* 33, pp. 9851–9864.

Experiments

Experimental Settings



- Experiments are performed on an in-house 64-bit high-performance commercial microprocessor.
- VLSI flow is used to evaluate the PPA values of each microarchitecture configuration.
- Register-transfer-level (RTL) design: generated from Chisel⁴.
- Performance and power: obtained from the benchmark simulation after the physical implementation.
- Area value: reported from the physical implementation tool.
- Due to the commercial confidentiality of microarchitecture, we normalize the original PPA values.

⁴Jonathan Bachrach et al. (2012). "Chisel: Constructing Hardware in a Scala Embedded Language". In: *ACM/IEEE Design Automation Conference (DAC)*, pp. 1216–1225.

Transfer Design Space Exploration Experiment Setup



Module	# Linear	#Pow	# Categorical	# Combinations
IFU	8	4	0	$\sim 4 imes 10^8$
OoO	11	0	0	$\sim 5 imes 10^9$
IEX	12	0	3	$\sim 1 imes 10^9$
FSU	5	0	0	$\sim 2 imes 10^3$
LSU	6	3	0	$\sim 1 imes 10^7$
L2C	1	2	0	$\sim 8 imes 10^2$
Overall	43	9	3	$\sim 3 imes 10^{40}$

Table: Statistics of our microarchitecture design space

- 55 parameters with more than 3×10^{40} combinations.
- Task A, task B, and Task C share the same design space, for task D, we can only tune 53 parameters out of 55 parameters listed.
- Task A, task B, task C, and Task D contain 1237, 377, 1835 and 3453 evaluated samples, respectively.



Data distribution for 4 tasks.



Hypervolume:

$$HV_{y_{ref}}(\mathcal{P}(\boldsymbol{y})) = \lambda_M(\cup_{\boldsymbol{y}\in\mathcal{P}(\boldsymbol{y})}[\boldsymbol{y},\boldsymbol{y}_{ref}]), \tag{18}$$

Average distance to reference set (ADRS):

$$ADRS(\mathcal{P}^*, \mathcal{P}) = \frac{1}{|\mathcal{P}^*|} \sum_{\alpha \in \mathcal{P}^*} \min_{\beta \in \mathcal{P}} l_2(\alpha, \beta),$$
(19)



	A, B \rightarrow C		$A, C \rightarrow B$		$B, C \rightarrow A$	
Methodologies	ADRS	HV	ADRS	HV	ADRS	HV
Ground Truth	0.0	0.0984	0.0	0.0684	0.0	0.0809
ANN-TL ⁵	0.069	0.0891	0.045	0.0643	0.031	0.0749
Deep-Ens ⁶	0.072	0.0840	0.066	0.0599	0.055	0.0727
ERM ⁷	0.080	0.0877	0.064	0.0629	0.043	0.0742
IRMv1 ⁸	0.025	0.0938	0.023	0.0667	0.027	0.0766
Ours	0.021	0.0944	0.017	0.0679	0.020	0.0796

Table: Comparison of transfer performance in same design space

⁵Jianwang Zhai, Yici Cai, and Bei Yu (2023). "Microarchitecture Power Modeling via Artificial Neural Network and Transfer Learning". In: 2023 28th Asia and South Pacific Design Automation Conference (ASP-DAC), pp. 1–6.

⁶Balaji Lakshminarayanan, Alexander Pritzel, and Charles Blundell (2017). "Simple and scalable predictive uncertainty estimation using deep ensembles". In: *Advances in neural information processing systems* 30.

⁷VN Vapnik (1992). Principles of risk minimization for learning theory, Advances in Neural Information *Processing NIPS 4 (pp. 831±838).*

⁸Martin Arjovsky et al. (2019). "Invariant risk minimization". In: *arXiv preprint arXiv:*1907.02893. 27/30

Pareto-optimal Sets Visulation





Pareto-optimal sets using source task data from same design space. Left: performance versus area; right: power versus area.

Transferring Performance within Different Design Space



To the best of our best knowledge, all previous methods cannot spread knowledge across different design spaces. This serves as an ablation study.

Table: Comparison of transfer performance in different design spaces

	A, D \rightarrow C		A, $C \rightarrow D$	
Methodologies	ADRS	HV	ADRS	HV
Ground Truth	0.0	0.0984	0.0	0.7792
w/o. Pre-train	0.0533	0.0857	0.0853	0.7465
w/o. Ensemble	0.0275	0.0892	0.0722	0.7531
w/o. IRM	0.0293	0.0910	0.0701	0.7569
w/. IRMv1	0.0232	0.0914	0.0687	0.7602
Ours	0.0217	0.0924	0.0641	0.7624

THANK YOU!