



香港中文大學

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Lab 1-3: RISC-V Assembly Language Programming III

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Outline

① Recap

② Recursive Program in RISC-V Assembly

③ Quicksort

④ Lab 1-3 Assignment

Recap

Example 1 – Array Definition I

Example

```
.data  
    a: .word 1 2 3 4 5
```

Example 2 – If-ElseIf-Else Statement I

Example

Example 2 – If-ElseIf-Else Statement II

```
_start:  
    andi t0, t0, 0          # clear register t0  
    andi t1, t1, 0          # clear register t1  
    andi t2, t2, 0          # clear register t2  
    andi t3, t3, 0          # clear register t3  
    andi t4, t4, 0          # clear register t4  
    andi t5, t5, 0          # clear register t5  
    li t0, 2                # t0 = 2  
    li t3, -2               # t3 = -2  
    slt t1, t0, zero        # t1 = t0 < 0 ? 1 : 0  
    beq t1, zero, ElseIf   # go to ElseIf if t1 = 0  
    j EndIf                 # end If statement  
ElseIf:  
    sgt t4, t3, zero        # t4 = t3 > 0 ? 1 : 0  
    beq t4, zero, Else      # go to Else if t4 = 0  
    j EndIf                 # end Else statement  
Else:  
    seqz t5, t4, zero       # t5 = t4 == 0 ? 1 : 0  
EndIf:  
    j EndIf                 # end If-ElseIf-Else statement
```

Example 3 – While Loop I

Example

```
_start:  
    andi t0, t0, 0          # clear register t0  
    andi t1, t1, 0          # clear register t1  
    andi t2, t2, 0          # clear register t2  
    li t1, 100              # t1 = 100  
loop:  
    add t2, t2, t0          # t2 = t2 + t0  
    addi t0, t0, 1          # ++t0  
    blt t0, t1, loop        # iterate if t0 < t1  
end:  
    j end                  # end of While loop
```

Example 4 – For Loop I

Example

```
_start:  
    andi t0, t0, 0          # clear register t0  
    andi t1, t1, 0          # clear register t1  
loop:  
    andi t2, t2, 0          # clear t2 before starting the loop  
    add t1, t1, t0           # t1 = t1 + t0  
    addi t0, t0, 1            # ++t0  
    slti t2, t0, 100         # t2 = t0 < 100 ? 1 : 0  
    bne t2, zero, loop       # go to loop if t2 != 0  
end:  
    j end                  # end of For loop
```

Recursive Program in RISC-V Assembly

Compiling a Recursive Program

A procedure for calculating factorial

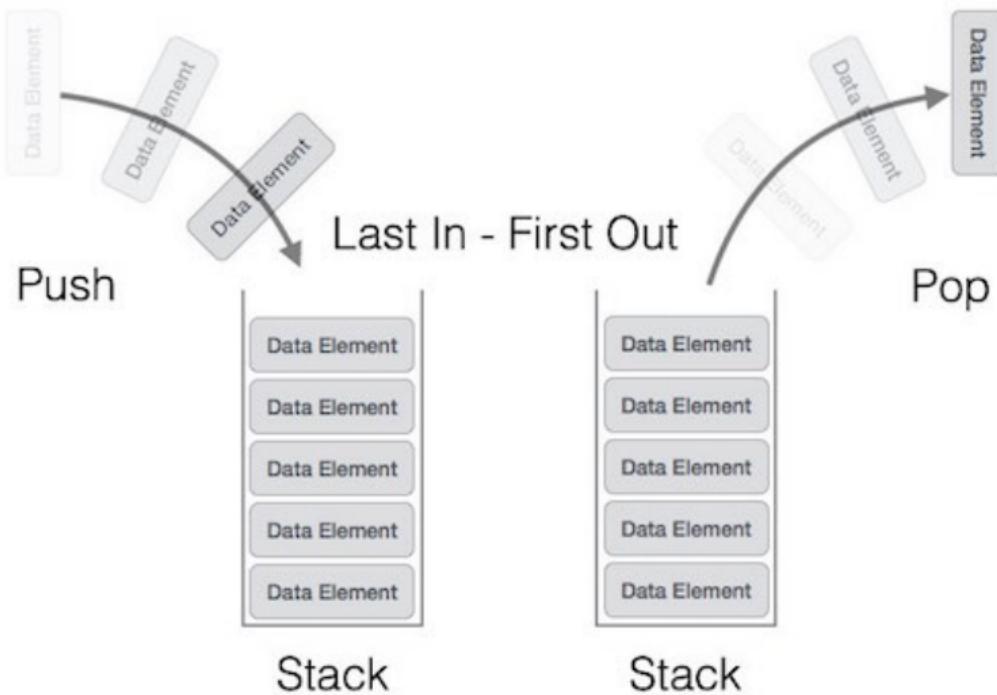
```
int fact (int n)
{
    if (n < 1) return 1;
    else return (n * fact (n-1));
}
```

- A recursive procedure (one that calls itself!)

```
fact (0) = 1
fact (1) = 1 * 1 = 1
fact (2) = 2 * 1 * 1 = 2
fact (3) = 3 * 2 * 1 * 1 = 6
fact (4) = 4 * 3 * 2 * 1 * 1 = 24
. . .
```

- Assume n is passed in `a0`; result returned in `ra`

Stack



Compiling a Recursive Program (cont.)

```
fact:  
    addi  sp, sp, -8      # adjust the stack pointer  
    sw    ra, 4(sp)       # save the return address  
    sw    a0, 0(sp)       # save the argument n  
    slti  t0, a0, 1       # test for n < 1  
    beq   t0, zero, L1    # if n >=1, go to L1  
    addi  t1, zero, 1     # else return 1 in t1  
    addi  sp, sp, 8       # adjust stack pointer  
    jr    ra               # return to caller  
  
L1:  
    addi  a0, a0, -1     # n >=1, so decrease n  
    jal   fact             # call fact with (n-1)  
                            # this is where fact returns  
  
bk_f:  
    lw    a0, 0(sp)       # restore argument n  
    lw    ra, 4(sp)       # restore return address  
    addi  sp, sp, 8       # adjust stack pointer  
    mul   t1, a0, t1     # t1 = n * fact(n-1)  
    jr    ra               # return to caller
```

Another Example I

Example

Another Example II

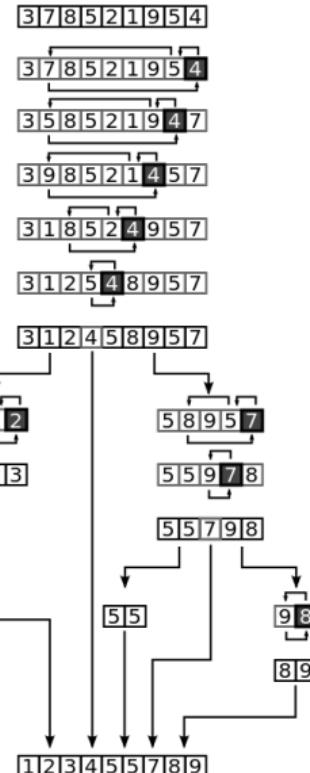
```
.globl _start
.text
fact:
    addi sp, sp, -8          # recursive implementation of factorial
    sw ra, 0(sp)            # arg: n in a0, returns n! in a1
    li t0, 2                 # reserve our stack area
    blt a0, t0, ret_one     # save the return address
    sw a0, 4(sp)             # t0 = 2
                            # go to ret_one if a0 < t0
    addi a0, a0, -1          # save our n
    jal fact                # call fact (n-1), a1 <- fact(n-1)
    lw t0, 4(sp)             # t0 <- n
    mul a1, t0, a1           # a1 <- n * fact(n-1)
    j done
ret_one:
    li a1, 1
done:
    lw ra, 0(sp)            # restore return address from stack
    addi sp, sp, 8            # free our stack frame
    jr ra                   # and return
_start:
    li a0, 5                 # compute 5!
    jal fact                # call 'fact'
    li a0, 1                 # print it
    ecall
```

Quicksort

Quicksort

Overview of Quicksort

Quicksort is a **divide and conquer** algorithm. Quicksort first divides a large array into two smaller sub-arrays: the low elements and the high elements. Quicksort can then recursively sort the sub-arrays.

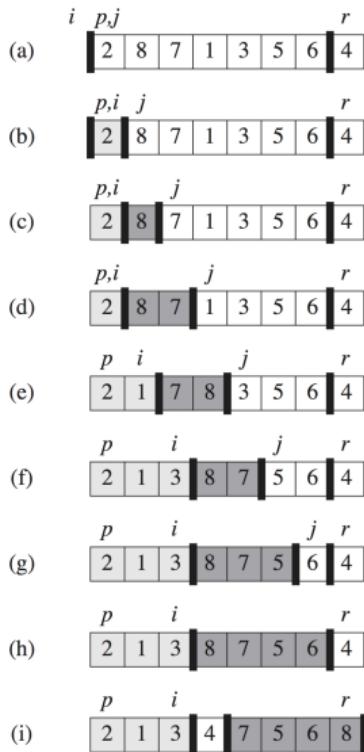


Quicksort: Array Partitioning (Lab 1-2)

- Pick an element, called a pivot, from the array.
- Reorder the array so that all elements with values less than the pivot come before the pivot, while all elements with values greater than the pivot come after it (equal values can go either way).

```
1: function PARTITION(A, lo, hi)
2:   pivot  $\leftarrow$  A[hi]
3:   i  $\leftarrow$  lo-1;
4:   for j = lo; j  $\leq$  hi-1; j  $\leftarrow$  j+1 do
5:     if A[j]  $\leq$  pivot then
6:       i  $\leftarrow$  i+1;
7:       swap A[i] with A[j];
8:     end if
9:   end for
10:  swap A[i+1] with A[hi];
11:  return i+1;
12: end function
```

Example of Array Partition



1

¹In this example, $p = \text{lo}$ and $r = \text{hi}$.

Quicksort: Sorting

- Recursively apply the array partition to the sub-array of elements with smaller values and separately to elements with greater values.

```
1: function QUICKSORT(A, lo, hi)
2:   if lo < hi then
3:     p ← partition(A, lo, hi);
4:     quicksort(A, lo, p - 1);
5:     quicksort(A, p + 1, hi);
6:   end if
7: end function
```

Lab 1-3 Assignment

Lab Assignment

Implement Quicksort *w.r.t.* the following array in ascending order:

Sort the array for this assignment

-1 22 8 35 5 4 11 2 1 78

Submission Method:

Submit the source code and report into **Blackboard**, including

- All source codes (name-sid-lab1-x.asm, e.g., zhangsan-1234567890-lab1-1.asm, zhangsan-1234567890-lab1-2.asm, etc.)
- A lab report (name-sid-lab1.pdf) illustrates your implementation for three parts of Lab 1 and all console results (screenshots).
- Deadline: 23:59, 20 Feb (Sun)