

ARM Instruction set quick ref.: www.arm.com,

http://simplemachines.it/doc/QRC0001H_rvct_v2.1_arm.pdf

http://infocenter.arm.com/help/topic/com.arm.doc.qrc0001/QRC0001_UAL.pdf

Arithmetic	ADD{condition}{S} Rd, Rn, <Operand>
	ADC{condition}{S} Rd, Rn, <Operand>
	SUB{condition}{S} Rd, Rn, <Operand>
	SBC{condition}{S} Rd, Rn, <Operand>
	RSB{condition}{S} Rd, Rn, <Operand>
	RSC{condition}{S} Rd, Rn, <Operand>
	MUL{condition}{S} Rd, Rm, Rs
	MLA{condition}{S} Rd, Rm, Rs, Rn
	UMULL{condition}{S} RdLo, RdHi, Rm, Rs
	UMLAL{condition}{S} RdLo, RdHi, Rm, Rs
	SMULL{condition}{S} RdLo, RdHi, Rm, Rs
	SMLAL{condition}{S} RdLo, RdHi, Rm, Rs
Move	MOV{condition}{S} Rd, <Operand>
	MVN{condition}{S} Rd, <Operand>
Logical	TST{condition} Rn, <Operand>
	TEQ{condition} Rn, <Operand>
	AND{condition}{S} Rd, Rn, <Operand>
	EOR{condition}{S} Rd, Rn, <Operand>
	ORR{condition}{S} Rd, Rn, <Operand>
Compare	BIC{condition}{S} Rd, Rn, <Operand>
	CMP{condition} Rn, <Operand>
Branch	CMN{condition} Rn, <Operand>
	B{condition} label
Load / Store	BL{condition} label
	LDR{condition} Rd, <Addressing_model>
Stack	STR{condition} Rd, <Addressing_model>
	LDM{condition}<Addressing_mode2> Rn{!}, <register_list>
Software Interrupt	STM{condition}<Addressing_mode2> Rn{!}, <register_list>
	SWI{condition} #<immediate_value>

{S} means : Updates condition flags if S present

Required fields

<Operand>	
Immediate value	#<immediate_value>
Register	Rm
Logical shift left	Rm, LSL #<shift>
Logical shift right	Rm, LSR #<shift>
Arithmetic shift right	Rm, ASR #<shift>
Rotate right	Rm, ROR #<shift>
<Addressing_model>	
Zero offset	[Rn]
Immediate offset	[Rn, #+/-< immediate_value>]
Register offset	[Rn, +/-Rm]
Post-indexed Immediate offset	[Rn], #+/-< immediate_value>
Post-indexed Register offset	[Rn], +/-Rm
Pre-indexed Immediate offset	[Rn, #+/-< immediate_value>]!
Pre-indexed Register offset	[Rn, +/-Rm]!
<Addressing_mode2>	
Full Descending	FD
Empty Descending	ED
Full Ascending	FA
Empty Ascending	EA

Optional fields {condition}

EQ	Equal to zero (Z=1)	HI	Unsigned higher
NE	Not equal to zero (Z=0)	LS	Unsigned lower or same
MI	Negative	GE	Signed greater than or equal
PL	Positive or zero	LT	Signed less than
VS	Overflow	GT	Signed greater than
VC	No overflow	LE	Signed less than or equal